(12) INTERNATIONAL APPLICATION PUBLISHED UNDER THE PATENT COOPERATION TREATY (PCT)

(19) World Intellectual Property Organization International Bureau



- 1 (1841) 1 (1841) 1 (1841) 1 (1841) 1 (1841) 1 (1841) 1 (1841) 1 (1841) 1 (1841) 1 (1841) 1 (1841) 1 (1841) 1

(43) International Publication Date 29 January 2004 (29.01.2004)

PCT

(10) International Publication Number WO 2004/009729 A1

(51) International Patent Classification⁷: H01L 51/30, H05B 33/08

C09K 11/06,

(21) International Application Number:

PCT/IB2003/003014

(22) International Filing Date:

8 July 2003 (08.07.2003)

(25) Filing Language:

English

(26) Publication Language:

English

(30) Priority Data: 02077998.9

23 July 2002 (23.07.2002) EF

(71) Applicant (for all designated States except US): KONIN-KLIJKE PHILIPS ELECTRONICS N.V. [NL/NL]; Groenewoudseweg 1, NL-5621 BA Eindhoven (NL).

(72) Inventors; and

- (75) Inventors/Applicants (for US only): GIRALDO, Andrea [IT/NL]; c/o Prof. Holstlaan 6, NL-5656 AA Eindhoven (NL). JOHNSON, Mark, T. [GB/NL]; c/o Prof. Holstlaan 6, NL-5656 AA Eindhoven (NL).
- (74) Agent: DEGUELLE, Wilhelmus, H., G.; Philips Intellectual Property & Standards, Prof. Holstlaan 6, NL-5656 AA Eindhoven (NL).
- (81) Designated States (national): AE, AG, AL, AM, AT, AU, AZ, BA, BB, BG, BR, BY, BZ, CA, CH, CN, CO, CR, CU, CZ, DE, DK, DM, DZ, EC, EE, ES, FI, GB, GD, GE, GH, GM, HR, HU, ID, IL, IN, IS, JP, KE, KG, KP, KR, KZ, LC, LK, LR, LS, LT, LU, LV, MA, MD, MG, MK, MN, MW, MX, MZ, NI, NO, NZ, OM, PG, PH, PL, PT, RO, RU, SC,

SD, SE, SG, SK, SL, SY, TJ, TM, TN, TR, TT, TZ, UA, UG, US, UZ, VC, VN, YU, ZA, ZM, ZW.

(84) Designated States (regional): ARIPO patent (GH, GM, KE, LS, MW, MZ, SD, SL, SZ, TZ, UG, ZM, ZW), Eurasian patent (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM), European patent (AT, BE, BG, CH, CY, CZ, DE, DK, EE, ES, FI, FR, GB, GR, HU, IE, IT, LU, MC, NL, PT, RO, SE, SI, SK, TR), OAPI patent (BF, BJ, CF, CG, CI, CM, GA, GN, GQ, GW, ML, MR, NE, SN, TD, TG).

Declaration under Rule 4.17:

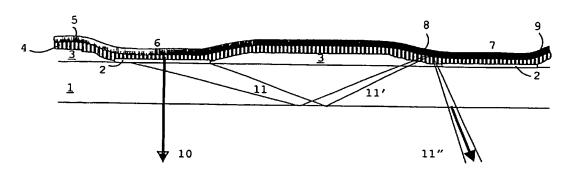
as to applicant's entitlement to apply for and be granted a patent (Rule 4.17(ii)) for the following designations AE, AG, AL, AM, AT, AU, AZ, BA, BB, BG, BR, BY, BZ, CA, CH, CN, CO, CR, CU, CZ, DE, DK, DM, DZ, EC, EE, ES, FI, GB, GD, GE, GH, GM, HR, HU, ID, IL, IN, IS, JP, KE, KG, KP, KR, KZ, LC, LK, LR, LS, LT, LU, LV, MA, MD, MG, MK, MN, MW, MX, MZ, NI, NO, NZ, OM, PG, PH, PL, PT, RO, RU, SC, SD, SE, SG, SK, SL, SY, TJ, TM, TN, TR, TT, TZ, UA, UG, UZ, VC, VN, YU, ZA, ZM, ZW, ARIPO patent (GH, GM, KE, LS, MW, MZ, SD, SL, SZ, TZ, UG, ZM, ZW), Eurasian patent (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM), European patent (AT, BE, BG, CH, CY, CZ, DE, DK, EE, ES, FI, FR, GB, GR, HU, IE, IT, LU, MC, NL, PT, RO, SE, SI, SK, TR), OAPI patent (BF, BJ, CF, CG, CI, CM, GA, GN, GQ, GW, ML, MR, NE, SN, TD, TG)

Published:

with international search report

For two-letter codes and other abbreviations, refer to the "Guidance Notes on Codes and Abbreviations" appearing at the beginning of each regular issue of the PCT Gazette.

(54) Title: ELECTROLUMINESCENT DISPLAY AND ELECTRONIC DEVICE COMPRISING SUCH A DISPLAY



(57) Abstract: The invention relates to an electroluminescent display comprising a first display pixel and a second display pixel formed on a substrate. The display pixels comprise a first electrode deposited on or across the substrate, an electroluminescent layer and a second reflective electrode. The first and second display pixels are separated by a region comprising at least one insulating structure. The insulating structure is adapted to suppress the output of light at the second display pixel reflected at the second reflective electrode, which light is incident from at least the first display pixel and/or the substrate. The insulating structure reduces crosstalk of light between the first and second or further display pixel and can be easily integrated in the manufacturing process of the electroluminescent display.